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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10054823	FILING DATE 01/25/2002	CLASS 257	SUBCLASS -7	GAU 281	EXAMINER <i>Greene</i>
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**APPLICANTS: Matsumoto Hironori; Ohmi Toshinori;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-027128 02/02/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 0397-0439-P
TITLE : Integrated semiconductor circuit device, process of manufacturing the same, IC module and IC card			
U.S. DEPT. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE		Application Examiner
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